

MATRIX THEORY CONFERENCE

A conference on Matrix Theory and its Applications was held in University College Dublin on March 22-24. The conference was organized by F.J. Gaines and T.J. Laffey, and was sponsored by the Department of Mathematics, U.C.D., the Irish Mathematical Society and the Symposium Fund of the Royal Irish Academy. It was well-attended, with about 35 participants. The lectures and discussion covered the many aspects of matrix theory - algebraic, analytic, combinatorial and computational - as well as its applications in applied mathematics. There was also a stimulating problem session, with several challenging questions.

The lecturers in order of appearance were as follows:

- Professor G.N. De Oliveira (Coimbra) *Matrices over finite fields*
- Professor T.T. West (T.C.D.) *Left/Right Symmetry in semi-simple algebras*
- Professor T.J. Laffey (U.C.D.) *Factorization of matrices as products of Skew-symmetrics*
- Professor R. Grone (Auburn, Alabama) *Computation of an immanant*
- Dr R. Timoney (T.C.D.) *Reinhardt decompositions of operator matrix spaces*
- Dr D.W. Lewis (U.C.D.) *Hermitian forms and von Neumann regular matrices*
- Dr E.P. O'Reilly (N.I.H.E. Dublin) *The recursion method - a matrix technique in solid state physics*
- Dr N.B. Blackhouse (Liverpool) *Grassman matrices*
- Professor F. Holland (U.C.C.) *Counterparts of Hankel and Toeplitz operators on C^p*
- Dr R. Gow (U.C.D.) *Some properties of unitary matrices*
- Dr D. O'Connor (U.C.D.) *Sparse matrices*
- Professor H. Wimmer (Wurzburg) *The algebraic Riccati equation*

T. Laffey

REPORT OF THE NASECODE III CONFERENCE

(Communicated by J.J.H. Miller of the Numerical Analysis Group, Dublin)

The third international conference on the Numerical Analysis of Semiconductor Devices and Integrated Circuits, NASECODE III, was held in Galway, Ireland, from June 15th to 17th, 1983, under the auspices of the Numerical Analysis Group. It was attended by over 120 delegates from 18 countries. The aim of this series of conferences is the fostering of a fruitful exchange of ideas between electronic engineers and numerical analysts, who are using existing and developing new computer codes for semiconductor process, device and integrated circuit modelling.

As on previous occasions the industrial sector was strongly represented and it is our policy to ensure that the topics discussed at these conferences are relevant to the needs of industry. This ensures that the scientific and technical material presented at the conference is not only intellectually challenging, but also of great practical importance.

The application of numerical methods to semiconductor device modelling began about 17 years ago, and since then it has developed and broadened in scope very rapidly. To date relatively few professional numerical analysts have worked in this area, and consequently it is still a fertile source of stimulating unsolved problems of widely varying degrees of difficulty.

The models of technological importance are mainly in two space dimensions and they may also be time dependent. Typically, two or three nonlinear differential equations have to be solved on complicated domains with a variety of boundary conditions. Computational experience indicates that the systems are often very stiff.